

AMENDMENT TO ABSTRACT:

Please cancel the Abstract presently on file and substitute the following Abstract therefor.

ABSTRACT

A display driver capable of operating in any one of multiple gradient display modes drives a liquid crystal panel in which the first L common electrodes are simultaneously selected in a first selection period and successive groups of L common electrodes are simultaneously selected in successive selection periods. 8-bit display data for each segment electrode are simultaneously read from a display data RAM and latched. First and second sub-decoders decode divided upper and lower four bits of the 8-bit display data, respectively. In a first mode, the data is for a four gradient display for each of four pixels on each segment electrode. In a second mode, the data is for a two gradient display for each of eight pixels on each segment electrode.